A Concurrent Dual-Band Low Noise Amplifier for GNSS Receivers

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Abstract: In this paper, a new design of concurrent dual-band Low Noise Amplifier (LNA) for multi-band single-channel Global Navigation Satellite System (GNSS) receivers is proposed. This new structure is able to operate concurrently at frequency of 1.2 and 1.57 GHz. Parallel and series resonance parts are employed in the input matching in order to achieve concurrent performance. With respect to used pseudo-differential structure, LNA is basically a single-ended-to-differential conversion and it consequently has no need to balun. In addition, an inductively degenerated cascode approach is employed to have better simultaneous matching and Noise Figure (NF). Simulations are performed with TSMC 0.18 μ m technology in ADS software. Results analysis present that LNA achieves input matchings of -11.024 and -13.131 dB, NFs of 2.315 and 2.333 dB, gains of 26.926 and 27.576 dB, P_{-1dB} of -15.3 and -13 dBm, IIP3 of -0.9 and 2.2 dBm at 1.2 and 1.57 GHz, respectively. Besides, LNA consumes 8.32 mA DC current from a 1.8 V supply voltage.

Keywords: Concurrent Dual-Band, GNSS, Impedance Matching, LNA, Noise Figure.

1 Introduction

GNSS provides electronic equipment with positioning, velocity and time information [1]. It is noticeable that the system utilizes satellites for RF signal transmissions.

Global Positioning System (GPS), which was advanced by US military department, is now the only useable navigation system throughout the world. GPS satellites as a part of GPS system are always trying to send out three different kind of signals (L1, L2 and L5) during moving in the space [2]. Broadcasting radio signals over wireless channels through GPS satellites enables GPS receivers to figure out the precise location, speed and time in different weather conditions [3]. But the sensitivity and accuracy of GPS system to find the positions is really affected by special environment around the receivers. In fact, not only working in wooded areas, jamming and multi-path environment can decay the accuracy of GPS system, but also received signals in buildings or under tunnels also might be inaccurate. Therefore, employing the simple GPS system with single band reception is not able to provide appropriate availability and navigation accuracy [4].

On the other hand, GNSS systems, including

GLONASS by Russia, Galileo by European Union and Compass by china, have not reached out GPS capability in terms of global coverage yet [5]. Apart from coverage, however, low sensitivity of GNSS systems has always obtained users complaints [6]. In order to increase the sensitivity and precision, GNSS receivers have to support more than one GNSS system simultaneously [7, 8]. Since GNSS systems work with two frequency bands (1.2 and 1.5 GHz), they should have supported both of the bands. The GNSS signals specifications are listed in Table 1. Dual-band receivers are divided into two main groups [9]: 1) concurrent: simultaneous dual-band support and 2) non-concurrent: non-simultaneous dual-band support.

Due to more electronics components in nonconcurrent's high power consumption structures, it is not good candidate in modern electronics systems [10]. Concurrent dual-band receivers can support two bands through two channels [4, 11-17] or one channel [18,19]. Utilizing two channels receivers not only increases the power consumption, but it also expands the size and used area. Therefore, they are not considered as an efficient approach, but receiving two bands from just one channel can optimize power consumption and area consumption. As an alternative in two bands structures, using only one channel can optimize the power and area consumption and improves the system performance. That's why this kind of structures has been selected as a case study.

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Carrier BW Power Access System Band Freq. (dBW) Technique (MHz) (MHz) 1575.42 L1 C/A 2.046 -158.5 1227.6 L2 C 2.046 -164.5 GPS CDMA 1176.45 L5 20.46 -157.9 L11602 -161 GLONASS FDMA L2 1246 -167 4.092 E1 1575.42 -157 E5a 1176.45 20.46 -155 Galileo CDMA E5b 1207.14 20.46 -155 **B**1 1561.098 4.092 -163 BD-2 CDMA B2 1207.14 20.46 -163

Table 1 List of GNSS signals

In order to concurrent dual-band structure, we need a special frequency plan. There are some reasons which make the Low Noise Amplifier (LNA) one of the most important blocks of concurrent dual-band receiver, because of lower power in RF signal compared to thermal noise, have high gain and low Noise Figure (NF) in LNA causes designing process to be complicated. Also, LNA should support two frequency bands in one channel. Since LNA block plays an important role in receiver's structure, other parameters such as linearity, and power consumption should be optimized. As a result, LNA design is considered to be extremely important step in concurrent dual-band receivers design.

This paper is organized as followed. Section 2 describes the architecture of proposed concurrent dualband LNA. Section 3 would discuss simulation results. It also goes through a full analysis of simulation results and comparison with previous works. Section 4 gives a thorough conclusion of this paper.

2 LNA Architecture

Fig. 1 represents an overview of proposed concurrent dual-band LNA. The small-signal equivalent circuit of Fig. 1 is delineated in the Appendix. LNA structure is pseudo-differential amplifier with inductively degenerated common source topology.

The basic concept of the proposed circuit is relied on the differential amplifier with differential outputs and inputs as shown in Fig. 2. In order to eliminate the input balun, the mentioned structure can be modified to pseudo-differential.

The most important advantage of using singleended-to-differential conversion is front-end protection, particularly in high common mode interference situation. In addition, the more switching rate at the back end of the receiver makes this structure more effective. Other advantages are mentioned as follow: 1) decrease the feedthrough of the local oscillator at the mixer output and 2) removing off-chip balun because of LNA single input.

In more details, different parameters of the proposed case study are discussed below.



Fig. 1 Schematic of the proposed LNA.



Fig. 2 Differential amplifier.

2.1 Concurrent Dual-Band Matching

Matching is a significant part of LNA designing process in order to achieve the maximum power transmission. Different parts of the input matching circuit are depicted in Fig. 1. As can be seen in this figure, L₂, C₂ are placed in the parallel resonant part as well as L₁, L_s, and C_t (C_t=C_{ex} $||C_{gs})$ are employed in series resonant part. These two resonance parts provide simultaneous matching for two required frequency bands (1.2 and 1.57 GHz in this case).

Besides, in order to realize the input matching, the input impedance should be equal to conjugation of the source impedance:

$$Z_{opt} = Z_s^*$$
(1)

where Z_{S}^{*} is source impedance conjugate and Z_{opt} is optimal noise impedance.

To attain the small NF, the optimum noise source impedance can be determined as follow [20]: $Re[Z_{out}] =$

$$\frac{\alpha\sqrt{\frac{\delta}{5\gamma}\left(1-|c|^{2}\right)}}{\omega C_{gs1}\left\{\frac{\alpha^{2}\delta}{5\gamma}\left(1-|c|^{2}\right)+\left(\frac{C_{t}}{C_{gs1}}+\alpha|c|\sqrt{\frac{\delta}{5\gamma}}\right)^{2}\right\}}=R_{s}$$
(2)

where α is a constant value. δ and γ are gate induced noise and channel noise coefficients, respectively, and c is the correlation coefficient between the gate induced noise and channel noise.

Im[Z_{opt}] = j
$$\omega \left[\frac{L_2}{1 - \omega^2 L_2 C_2} + L_1 + L_3 - \frac{1}{\omega^2 C_1} \right] = 0$$
 (3)

in which C_t is the equivalent of parallel C_{ex} and C_{gs} capacitors (capacitors between gate and source of M1) providing simultaneous noise and input matching for two bands.

Based on Eq. (2) for one frequency band and by substituting ω for 1.57 GHz, C_{ex1} can be solved. In order to achieve input matching, following equation should be satisfied:

$$Z_{in} = Z_s^* \tag{4}$$

$$\operatorname{Re}[Z_{in}] = \frac{g_{m}L_{s}}{C_{t}} = R_{s}$$
(5)

$$Im[Z_{in}] = j\omega \left[\frac{L_2}{1 - \omega^2 L_2 C_2} + L_1 + L_s - \frac{1}{\omega^2 C_t} \right] = 0$$
(6)

where g_m is transconductance of M1. g_m is derived from bias current, which has been calculated by power constraint technique. Since Eq. (3) and Eq. (6) are equivalent, both of them can be used to determine L₁, L₂ and C₁. However, solving the equation for both frequencies (1.2 and 1.57 GHz) will result in several values for these parameters. Final determination should satisfy both parallel and series resonant parts.

2.2 Gain

The gain is adversely affected by the load, which is resistive in the proposed structure (Fig. 1). This makes the resonance frequencies solely dependent on the matching network. Finding this resistance is a major challenge in LNA design because of some intrinsic limitations. The upper bound is limited by the gain and the lower bound is limited by the resistance power and voltage. The proposed design has determined this resistance, so the voltage dropping is lower than cascode transistor's threshold. Therefore transistors are constantly in saturated region.

Gain can be determined from:

$$Gain = 2Q_{in}g_{m}R_{L}$$
⁽⁷⁾

where Q_{in} is the quality factor of input matching network. Because of the differential structure, the gain in Eq. (7) is multiplied by 2. It means that it should be increased by 6 dB.

2.3 Compensation Capacitor

Pseudo-differential structure, which is used in this paper, is not thoroughly symmetrical. Therefore, a capacitor is required to equalize the current of both sides in terms of magnitude and phase. This capacitor, which is called compensation capacitor, injects current to the right side and compensates the difference of both sides. The injected current can be expressed:

$$i_{injected} = g_{m2} V_{G2}$$
(8)

$$V_{G2} = \frac{C_{c}}{C_{c} + C_{gs2}} V_{D1}$$
(9)

$$V_{\rm D1} = -\frac{Q_{\rm in}g_{\rm m1}V_{\rm s}}{g_{\rm m3}}$$
(10)

So that, V_{G2} is the gate voltage of M2, V_{D1} is the drain voltage of M1 and V_S is the source voltage.

From the above equations, the value of the C_C is determined so that, the extra current compensates the decreasing current at the half right of the circuit. In order to analyze the effect of C_C in the proposed circuit, the drain current is depicted in the Figs. 3 and 4.

3 Simulation Results

A new design of concurrent dual-band LNA which is able to operate at both required frequency bands (1.2 and 1.57 GHz) is proposed in this study. Simulations are performed in TSMC 0.18 μ m technology in ADS software.

To analysis of the input matching, the S11 parameter is demonstrated in Fig. 5. As can be seen in this figure, S11 can reach -11.024 and -13.131 dB in 1.2 and 1.57 GHz, respectively. In addition, It is clear that matching occurs simultaneously only in the mentioned operating frequencies and never happens in other frequencies.



Fig. 3 Drain currents without C_C



Fig. 4 Drain currents with C_C



Fig. 5 Simulation result of S11 parameter.

Fig. 6 shows the gain curve for suggested LNA. Regarding the matching in two frequency bands, the gain has reached a reasonable amount. The gain values for 1.2 and 1.57 GHz are 26.926 and 27.576 dB, respectively. Due to less source negative feedback effect in higher frequencies, gain is higher in 1.57 GHz. As shown in this figure, the gain curve has formed a notch shape. This notch has occurred because of input mismatching and causes the attenuation of image and unwanted signals between these frequencies. Attenuation amount in these frequencies is about 18 dB.

To more investigation, the effect of cascode approach on miller effect and isolation is perceptible from Fig. 7. According to this figure, it is evident that S12 values in 1.2 and 1.57 GHz are -39.557 and -39.089 dB, respectively.

Fig. 8 depicts the NF diagram of a proposed LNA. Designed LNA has reached a much reliable amount in both frequency bands concurrently. Based on Fig. 8, NF, is 2.315 and 2.333 dB in 1.2 and 1.57 GHz, respectively. As can be seen in this figure, although the NF value between these two frequency bands reaches 25 dB, the gain attenuation is approximately 18 dB (Fig. 6) that prevents the unwanted signals to pass through circuit. Therefore, the circuit performance cannot be affected by high NF between this frequency ranges.



Fig. 6 Simulation result of gain parameter.



Fig. 7 Simulation result of S12 parameter.



Fig. 8 Simulation result of NF.

Regarding proper isolation between input and output, NF is only affected by the input matching. Avoiding balun in the input part of LNA is another factor to keep NF minimum.

Fig. 9 presents 1-dB compression point and linearity performance of the proposed LNA. P_{-1dB}, as shown in Fig. 9, is -15.3 and -13 dBm in 1.2 and 1.57 GHz, respectively. Using differential structure and balancing with compensation capacitor results in even harmonics' attenuation and perfect linearity in LNA's performance.

IIP3 of the designed LNA is presented in Fig. 10. With respect to this figure, its value is -0.9 and 2.2 dBm in 1.2 and 1.57 GHz, respectively. During concurrent operation, linearity is extremely important. Figs. 9 and 10 clearly prove that designed LNA has achieved the desired results in terms of linearity.

To evaluate the stability of the proposed structure, Rollett's parameters [21] are depicted in Figs. 11 and 12 based on the following equations:

$$\begin{cases} K = \frac{1 - |S_{11}|^2 - |S_{22}|^2 + |\Delta|^2}{2|S_{12}S_{21}|} \ge 1 \\ |\Delta| = |S_{11}S_{22} - S_{12}S_{21}| \le 1 \end{cases}$$
(11)

According to Fig. 11, stability constant (K) reaches 10.18 and 12.256 in 1.2 and 1.57 GHz, respectively. Fig. 12 shows that $|\Delta|$ is about 0.076 and 0.106 in these frequencies. So, it is self-evident that the Rollett's conditions are satisfied and the amplifier is unconditionally stable.



Fig. 9 Simulation result of $P_{\text{-1dB}}$ at (a) 1.2 GHz and (b) 1.57 GHz.



Fig. 10 Simulation result of IIP3 at (a) 1.2 GHz and (b) 1.57 GHz.



Fig. 11 Simulation result of stability factor (K).



Fig. 12 Simulation result of delta magnitude.

As shown in the Table 2, a comparative study is carried out between proposed LNA in this paper with previous works. Proposed LNA has concurrent dualband performance capability. Besides, differential structure has led to a better linearity and increased LNA's gain. Designed LNA has also reached an extremely reasonable amount in input matching and NF simultaneously in both bands.

Also, to show the effectiveness of the proposed structure a Figure Of Merit (FOM) is defined as follow:

$$FOM = \frac{Gain[lin].f[GHz]}{S11[lin].(NF[lin]-1).P[mW]}$$
(12)

Based on the Eq. (11), it is self-evident that greater value for FOM proves the better performance of circuit.

As can be seen in Table 2, the value of the FOM for the proposed structure is approximately 2.5 times and 8.7 times of FOM in [18] for 1.2 GHz and 1.57 GHz, respectively. Furthermore, the amount of FOM for proposed circuit increased by around 49% compared to FOM in [4] for 1.2 GHz. In comparison with [22], there is not only a reduction of about 46% in power consumption, but also there is a serious difference in FOM value (8.996 compared to 1.343 for 1.2 GHz and 15.97 compared to 2.044 for 1.57 GHz).

4 Conclusion

Concurrent dual-band LNA of a GNSS receiver, which works in two require frequency bands 1.2 GHz and 1.57 GHz, is designed and simulated in this paper.

Simulation results declare that LNA's performance achieves acceptable results in both frequency bands simultaneously.

Results include perfect input matching, gain, NF and linearity. Not only pseudo-differential structure has modified the gain, it also has minimized NF. Furthermore, parallel and series resonance parts have enhanced the input matching capability. As a result proposed LNA can be considered as a perfect choice for concurrent multi-band single-channel GNSS receiver.

Appendix

The small-signal equivalent circuit of Fig. 1 is demonstrated in Fig. 13.



Fig. 13 Small-signal of Fig. 1.

Table 2 Performance comparison

According to Fig. 11, the small-signal input impedance can be calculated by using Eqs. (13) to (18), respectively:

$$KVL: -V_{T} + \left[\left(jL_{2}\omega \right) \right] \left[\left(\frac{1}{jC_{2}\omega} \right) + jL_{1}\omega + \frac{1}{jC_{t}\omega} \right] I_{T}$$

$$+ jL_{s}\omega \left(I_{T} + g_{ml}V_{ml} \right) = 0$$
(13)

$$(jL_{2}\omega)\left\| \left(\frac{1}{jC_{2}\omega}\right) = \frac{(jL_{2}\omega)\left(\frac{1}{jC_{2}\omega}\right)}{(jL_{2}\omega) + \left(\frac{1}{jC_{2}\omega}\right)} = \frac{jL_{2}\omega}{1 - L_{2}C_{2}\omega^{2}} \quad (14)$$

$$V_{gs1} = \frac{1}{jC_t\omega} I_T$$
(15)

$$V_{T} = I_{T} \left[\frac{jL_{2}\omega}{1 - L_{2}C_{2}\omega^{2}} + jL_{1}\omega + \frac{1}{jC_{t}\omega} + jL_{s}\omega + \frac{jL_{s}\omega g_{m1}}{jC_{t}\omega} \right]$$
(16)

$$Z_{in} = \frac{V_{T}}{I_{T}}$$
(17)

$$Z_{in} = \left(\frac{g_{m1}L_{s}}{C_{t}}\right) + j\omega \left(\frac{L_{2}}{1 - \omega^{2}L_{2}C_{2}} + L_{1} + L_{s} - \frac{1}{\omega^{2}C_{t}}\right)$$
(18)

Parameter	Proposed structure	[18]	[4]	[22]
Process	0.18 µm CMOS	0.18 µm CMOS	0.18 µm CMOS	90 nm CMOS
Structure	Pseudo-differential	Single-ended-to- differential	Pseudo-differential	Differential
Concurrent dual-band	Yes	Yes	No	No
Supply voltage (V)	1.8	1.8	1.8	1
Power consumption (mW)	14.967	11.16	18	28
Frequency (GHz)	1.2/1.57	1.217/1.568	1.2/1.57	1.8/2
S11 (dB)	-11.024/-13.131	-10.6/-10.7	-12/-11	< -10
Gain (dB)	26.926/27.576	13/11.5	23/20	15/16
NF (dB)	2.315/2.333	1.58/3	2.1/2.3	4/3.5
P-1dB (dBm)	-15.3/-13	-14/-13.5	-	-
IIP3 (dBm)	-0.9/2.2	-	-5.7/-3.9	+11
FOM	8.996/15.97	3.619/1.817	6.032/4.431	1.343/2.044

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